

## 97-2 大葉大學 完整版課綱 - 上課進度

上課進度		分配時數(%)				
週次	教學內容	講授	示範	習作	實驗	其他
1	Digital Computers and Information	66	34			
2	Digital Computers and Information	66	34			
3	Digital Computers and Information	66	34			
4	Combinational Logic Circuits	66	34			
5	Combinational Logic Circuits	66	34			
6	Combinational Logic Circuits	66	34			
7	Combinational Logic Circuits	66	34			
8	Combinational Logic Design	66	34			
9	Combinational Logic Design	66	34			
10	Combinational Logic Design	66	34			
11	Combinational Logic Design	66	34			
12	Sequential Circuits	66	34			
13	Sequential Circuits	66	34			
14	Sequential Circuits	66	34			
15	Sequential Circuits	66	34			
16	Registers and Counters	66	34			
17	Memory and Programmable Logic Devices	66	34			

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