

## 99-2 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	高等計算機架構	Serial No. / ID	1774 / EDR5141
Dept.	電機工程學系博士班	School System / Class	研究所博士班1年1班
Lecturer	陳慶順	Full or Part-time	專任
Required / Credit	Optinal / 3	Graduate Class	No
Time / Place	(二)89A / H371	Language	Chinese

Introduction
This course is intended to study advanced computer architectures that utilize parallelism via multiple processing units and to understand the power and limitations of multiprocessor systems.

Outline
<ol style="list-style-type: none"><li>1. Introduction to Advanced Computer Architecture and Parallel Processing.</li><li>2. Multiprocessors Interconnection Networks.</li><li>3. Performance Analysis of Multiprocessor Architecture.</li><li>4. Shared Memory Architecture.</li></ol>

Prerequisite
Computer Architecture Verilog Hardware Description Language Digital Logic