

99-1 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	軟硬體協同設計	Serial No. / ID	3070 / IFR5133
Dept.	資訊工程學系碩士班	School System / Class	研究所碩士班1年1班
Lecturer	王欣平	Full or Part-time	專任
Required / Credit	Optinal / 3	Graduate Class	No
Time / Place	(二)34N / H705	Language	Chinese

Introduction	
NA	

Outline	
<ol style="list-style-type: none">1. Introduction to Co-Design2. RISC Processors and Instruction Set3. Architectures for Embedded Systems4. System Modeling and Specification5. Performance Modeling6. System-Level Synthesis7. Communication synthesis8. SOPC Platform	

Prerequisite	
NA	