

99-1 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	電路學(一)	Serial No. / ID	3034 / EEI2001
Dept.	電機工程學系	School System / Class	大學日間部2年2班
Lecturer	黃登淵	Full or Part-time	專任
Required / Credit	Required / 3	Graduate Class	No
Time / Place	(三)56 / H203 (四)6 / H203	Language	Chinese

Introduction

Introduce students to understand the analysis and design of direct current circuitry

Outline

1. Elements and variables
2. Resistive networks
3. Capacitive and inductive networks
4. Circuit theorems
5. Circuit analysis
6. 1st and 2nd order circuits

Prerequisite

Linear algebra and calculus