

99-1 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	邏輯設計	Serial No. / ID	1623 / MAV3013
Dept.	機械與自動化工程學系	School System / Class	四技部3年1班
Lecturer	張義芳	Full or Part-time	專任
Required / Credit	Optinal / 3	Graduate Class	No
Time / Place	(五)67 / H445 (四)1 / H445	Language	Chinese

Introduction
Logic circuit design is a fundamental course dealing with logic circuit design and development. This course will cover the principle of Boolean Algebra and Reduction Techniques. Lecture materials will provide students a clear and thorough understanding of the Boolean algebra and logic circuit design. The adequate ability to analyze the logic relation in the logic circuit system will be developed.

Outline
單元主題1： Number Systems and Codes 單元主題2： Basic Logic Gates 單元主題3： Boolean Algebra and Reduction Techniques 單元主題4： Exclusive-OR and Exclusive-NOR Gates 單元主題5： Flip-Flops and Registers

Prerequisite
Physics and Electric