# 99-1 Preliminary Syllabus, Da-Yeh Univ

## Information

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|---|---|---|---|
| Title | | Serial No. / ID | 1548 / MAI3001 |
| Dept. | | School System / Class | 3　5 |
| Lecturer | | Full or Part-time | |
| Required / Credit | Optinal / 3 | Graduate Class | No |
| Time / Place | (　)1　／H445 (　)67　／H445 | Language | Chinese |

## Introduction

Logic circuit design is a fundamental course dealing with logic circuit design and development. This course will cover the principle of Boolean Algebra and Reduction Techniques. Lecture materials will provide students a clear and thorough understanding of the Boolean algebra and logic circuit design. The adequate ability to analyze the logic relation in the logic circuit system will be developed.

## Outline

1. Number Systems and Codes
2. Basic Logic Gates
3. Boolean Algebra and Reduction Techniques
4. Exclusive-OR and Exclusive-NOR Gates
5. Flip-Flops and Registers

## Prerequisite

Physics and Electric