

99-1 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	數位信號處理架構設計	Serial No. / ID	1166 / EGR5359
Dept.	電機工程學系碩士班	School System / Class	研究所碩士班1年1班
Lecturer	陳木松	Full or Part-time	專任
Required / Credit	Optinal / 3	Graduate Class	No
Time / Place	(二)ABC / H367	Language	English

Introduction

Discuss how the digital signal processing algorithms can be applied to the design, implementation, and analysis of integrated circuits.

Outline

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Prerequisite

Linear Algebra, Probability theory, digital signal processing