

98-2 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	積體電路封裝及測試	Serial No. / ID	1815 / EEI4251
Dept.	電機工程學系	School System / Class	大學日間部4年1班
Lecturer	周鵬程	Full or Part-time	兼任
Required / Credit	Optinal / 3	Graduate Class	Yes
Time / Place	(二)5 / H467 (四)12 / H467	Language	Chinese

Introduction
Introduce students to understand how to do packaging and tests to integrated circuits. Understand the present status and companies how they involved.

Outline
<ol style="list-style-type: none">1. Structure of IC2. Digital IC and analog IC3. IC packaging4. IC specification reading5. IC electrical testing6. IC environmental testing

Prerequisite
Digital logic and electronics