

## 98-2 Preliminary Syllabus, Da-Yeh Univ

| Information       |                          |                       |                |
|-------------------|--------------------------|-----------------------|----------------|
| Title             | 電子電路                     | Serial No. / ID       | 1814 / EEI3078 |
| Dept.             | 電機工程學系                   | School System / Class | 大學日間部4年1班      |
| Lecturer          | 胡大湘                      | Full or Part-time     | 專任             |
| Required / Credit | Optinal / 3              | Graduate Class        | Yes            |
| Time / Place      | (一)3 / H719 (三)12 / H719 | Language              | Chinese        |

| Introduction   |
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| 1. circuit analysis and design<br>2. problem analysis systematically with mathmetics |

| Outline   |
|---|
| Bascis Concepts<br>Basic Laws<br>Methods of Analysis<br>Circuit Theorems<br>Operational Amplifiers<br>Capacitors and Inductors<br>Fisrt-Order Circuits<br>Second-Order Circuits |

| Prerequisite |
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| 微積分、普通物理     |