

98-2 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	電路學實驗	Serial No. / ID	1801 / EEI2035
Dept.	電機工程學系	School System / Class	大學日間部2年2班
Lecturer	洪士賢	Full or Part-time	專任
Required / Credit	Required / 1	Graduate Class	NO
Time / Place	(四)9AB / H370	Language	Chinese

Introduction

Electric Circuits Laboratory

Outline

Thevenin Theorem; Norton Theorem; Maximum Power Transfer; AC Voltage, Current, Power Measurement; RLC Series and Parallel Circuits.

Prerequisite

None