

98-2 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	高速數位系統設計	Serial No. / ID	1440 / EGR5284
Dept.	電機工程學系碩士班	School System / Class	研究所碩士班1年1班
Lecturer	吳俊德	Full or Part-time	專任
Required / Credit	Optinal / 3	Graduate Class	NO
Time / Place	(三)3 / H367 (四)34 / H367	Language	Chinese

Introduction
<ol style="list-style-type: none">1. A thorough introduction to the digital aspects of basic transmission line theory2. Crosstalk and nonideal transmission line effects on signal quality and timings3. The impact of packages, vias, and connectors on signal integrity4. The effects of nonideal return paths, high frequency power delivery, and simultaneous switching noise

Outline
<ol style="list-style-type: none">1. The Importance of Interconnect Design2. Ideal Transmission Line Fundamentals3. Crosstalk4. Nonideal Interconnect Issues5. Connectors, Packages, and Vias6. Nonideal Return Paths, Simultaneous Switching Noise, and Power Delivery

Prerequisite
Electromagnetics