

98-2 Preliminary Syllabus, Da-Yeh Univ

| Information | | | |
|-------------------|---------------|-----------------------|----------------|
| Title | 數位電路與邏輯設計 | Serial No. / ID | 0572 / EEI2011 |
| Dept. | 電機工程學系 | School System / Class | 大學日間部1年2班 |
| Lecturer | 李立民 | Full or Part-time | 專任 |
| Required / Credit | Required / 3 | Graduate Class | NO |
| Time / Place | (二)ABC / H341 | Language | Chinese |

| Introduction |
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| 1.digital I.C.introduction 2.boolean algebra and digital circuits 3.karnaugh map and digital circuit design 4.cobinational circuits design 5.sequentail logic I.C.introduction |

| Outline |
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| boolean algebra karnaugh maps multi-level circuits combinational circuit design logic devices latches and flip-flops |

| Prerequisite |
|---------------|
| logic algebra |