

# 98-1 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	數位信號處理架構設計	Serial No. / ID	3058 / EDR5190
Dept.	電機工程學系博士班	School System / Class	研究所博士班1年1班
Lecturer	陳木松	Full or Part-time	專任
Required / Credit	Optinal / 3	Graduate Class	NO
Time / Place	(一)AB / H365 (三)A / H377	Language	English

Introduction
Discuss how the digital signal processing algorithms can be applied to the design, implementation, and analysis of integrated circuits.

Outline
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Prerequisite
Linear Algebra, Probability theory, digital signal processing