

98-1 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	數位邏輯實驗	Serial No. / ID	1705 / EEI2037
Dept.	電機工程學系	School System / Class	大學日間部2年3班
Lecturer	李世鴻	Full or Part-time	專任
Required / Credit	Required / 1	Graduate Class	NO
Time / Place	(二)9AB / H370	Language	Chinese

Introduction
1.digital I.C. 2.boolean algebra and digital circuits 3.karnaugh map and digital circuit design 4.cobinational circuits design 5.sequentail logic I.C.

Outline
boolean algebra karnaugh maps multi-level circuits combinational circuit design logic devices latches and flip-flops

Prerequisite
digital circuit and logic design