

98-1 Preliminary Syllabus, Da-Yeh Univ

| Information | | | |
|-------------------|---------------------------|-----------------------|----------------|
| Title | 實體設計研究 | Serial No. / ID | 1275 / IF14047 |
| Dept. | 資訊工程學系 | School System / Class | 大學日間部4年3班 |
| Lecturer | 程仲勝 | Full or Part-time | 專任 |
| Required / Credit | Optinal / 3 | Graduate Class | Yes |
| Time / Place | (四)5 / H705 (五)234 / H705 | Language | Chinese |

| Introduction | |
|--------------|--|
| NA | |

| Outline | |
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| <ol style="list-style-type: none">1. Introduction to VLSI Physical Design Automation2. Design and Fabrication of VLSI Devices3. Basic Algorithms4. Partitioning5. Floorplanning and Placement6. Routing7. Compaction8. Physical Design Automation of FPGAs and SoCs9. Some Related Topics | |

| Prerequisite | |
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| NA | |