

# 98-1 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	電路學(一)	Serial No. / ID	1044 / EEI2001
Dept.	電機工程學系	School System / Class	大學日間部2年1班
Lecturer	黃登淵	Full or Part-time	專任
Required / Credit	Required / 3	Graduate Class	NO
Time / Place	(二)34 / H203 (四)2 / H203	Language	Chinese

Introduction
Introduce students to understand the analysis and design of direct current circuitry

Outline
<ol style="list-style-type: none"><li>1. Elements and variables</li><li>2. Resistive networks</li><li>3. Capacitive and inductive networks</li><li>4. Circuit theorems</li><li>5. Circuit analysis</li><li>6. 1st and 2nd order circuits</li></ol>

Prerequisite
Linear algebra and calculus