

# 98-1 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	計算機系統結構	Serial No. / ID	0523 / IFR5003
Dept.	資訊工程學系碩士班	School System / Class	研究所碩士班1年1班
Lecturer	王欣平	Full or Part-time	專任
Required / Credit	Optinal / 3	Graduate Class	NO
Time / Place	(三)234 / H708	Language	Chinese

Introduction	
NA	

Outline	
Ch1 Introduction	
Ch2 Fundamentals of Computer Design : Design guide lines and performance evaluation	
Ch3 Instruction Set Principle and Example: Comparison of RISC and CISC architecture	
Ch4 Pipelining: Pipeline desing	
Ch5 Memory-Hierarchy Design: Cach types and design	
Ch6 Storage System	
Ch7 Interconnection Networks	

Prerequisite	
NA	