

97-2 Preliminary Syllabus, Da-Yeh Univ

| Information | | | |
|-------------------|---------------------------|-----------------------|----------------|
| Title | 數位晶片設計 | Serial No. / ID | 1325 / IFR5122 |
| Dept. | 資訊工程學系碩士班 | School System / Class | 研究所碩士班1年1班 |
| Lecturer | 林浩仁 | Full or Part-time | 兼任 |
| Required / Credit | Optinal / 3 | Graduate Class | NO |
| Time / Place | (二)56 / H705 (四)AB / H705 | Language | Chinese |

| Introduction | |
|--------------|--|
| NA | |

| Outline | |
|---|--|
| Unit 1 HDL Coding Styles for Synthesis Unit 2 Synthesis of Combinational Logic Unit 3 Synthesis of Sequential Logic Unit 4 Design and synthesis of datapath controllers Unit 5 Postsynthesis Design Tasks Unit 6 Final Project | |

| Prerequisite | |
|--------------|--|
| NA | |