97-2 Preliminary Syllabus, Da-Yeh Univ

Information

Title		Serial No. / ID	1298 / EGR 5325
Dept.		School System / Class	1 1
Lecturer		Full or Part-time	
Required / Credit	Optinal / 3	Graduate Class	NO
Time / Place	( )89A / H726	Language	Chinese

Introduction

This course is intended to study advanced computer architectures that utilize parallelism via multiple processing units and to understand the power and limitations of multiprocessor systems.

Outline

1. Introduction to Advanced Computer Architecture and Parallel Processing.
2. Multiprocessors Interconnection Networks.
3. Performance Analysis of Multiprocessor Architecture.
4. Shared Memory Architecture.

Prerequisite

Computer Architecture
Verilog Hardware De   ion Language
Digital Logic