

97-2 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	數位晶片設計	Serial No. / ID	0944 / IF13097
Dept.	資訊工程學系	School System / Class	大學日間部3年3班
Lecturer	林浩仁	Full or Part-time	兼任
Required / Credit	Optinal / 3	Graduate Class	NO
Time / Place	(二)56 / H705 (四)AB / H705	Language	Chinese

Introduction	
NA	

Outline	
Unit 1 HDL Coding Styles for Synthesis Unit 2 Synthesis of Combinational Logic Unit 3 Synthesis of Sequential Logic Unit 4 Design and synthesis of datapath controllers Unit 5 Postsynthesis Design Tasks Unit 6 Final Project	

Prerequisite	
NA	