

97-2 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	VLSI設計概論	Serial No. / ID	0942 / IF14008
Dept.	資訊工程學系	School System / Class	大學日間部3年3班
Lecturer	程仲勝	Full or Part-time	專任
Required / Credit	Optinal / 3	Graduate Class	NO
Time / Place	(五)234 / H726	Language	Chinese

Introduction	
NA	

Outline	
<ol style="list-style-type: none">1.Introduction to Digital Systems and VLSI2.Introduction to CAD Systems and Algorithms3.Introduction to VLSI Design Methodologies4.Introduction to Transistor Level Design5.Introduction to Gate Level Design6.Introduction to Architecture Level Design7.Introduction to Chip Level Design	

Prerequisite	
NA	