

97-1 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	數位訊號處理導論	Serial No. / ID	1449 / EEI4022
Dept.	電機工程學系	School System / Class	大學日間部3年1班
Lecturer	李立民	Full or Part-time	專任
Required / Credit	Optinal / 3	Graduate Class	NO
Time / Place	(一)78 / H303 (二)2 / H303	Language	Chinese

Introduction
This course will introduce the basic concepts for processing discrete-time signal. By the end of this course, the students should be able to analyze a DSP system and can use software tools to design simple DSP systems.

Outline
<ol style="list-style-type: none">1. sine wave & phasor2. spectrum representation3. sampling, alias, and reconstruction4. FIR filter5. linear and time-invariant6. FIR frequency response7. Z-transform8. IIR filter9. IIR Frequency Response10. three views of IIR filters

Prerequisite
Introduction to Computer