97-1 Preliminary Syllabus, Da-Yeh Univ

Information

Title	DSP ( )	Serial No. / ID	1303 / EDR 5040
Dept.		School System / Class	1 1
Lecturer		Full or Part-time	
Required / Credit	Optinal / 3	Graduate Class	NO
Time / Place	( )234 / H731	Language	Chinese

Introduction

1. Provide an understanding of the fundamentals, implementation, and application of DSP techniques from a practical point of view.
2. Provide a practical guidance to design and develop actual DSP systems.

Outline

To begin with, this course will introduce an understanding of the fundamentals, implementation, and application of DSP techniques from a practical point of view. And then PC-based Matlab will be adopted for the design and simulation for DSP algorithms. Finally, the VHDL implementation for DSP algorithms will be demonstrated.

Prerequisite

Digital Signal Processing