

97-1 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	電路學(一)	Serial No. / ID	0629 / EEI2001
Dept.	電機工程學系	School System / Class	大學日間部2年1班
Lecturer	黃登淵	Full or Part-time	專任
Required / Credit	Required / 3	Graduate Class	NO
Time / Place	(一)78 / H202 (二)5 / H202	Language	Chinese

Introduction	
Introduce students to understand the analysis and design of direct current circuitry	

Outline	
<ol style="list-style-type: none">1. Elements and variables2. Resistive networks3. Capacitive and inductive networks4. Circuit theorems5. Circuit analysis6. 1st and 2nd order circuits	

Prerequisite	
Linear algebra and calculus	