

# 103-2 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	FPGA晶片設計與應用實務	Serial No. / ID	2205 / EEB4021
Dept.	電機工程學系	School System / Class	進修學士班4年1班
Lecturer	黃登淵	Full or Part-time	專任
Required / Credit	Required / 3	Graduate Class	Yes
Time / Place	(二)ABC / H371	Language	Chinese

Introduction	
n.a.	

Outline	
n.a.	

Prerequisite	
n.a.	