

# 103-2 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	數位系統設計	Serial No. / ID	1078 / IFI3004
Dept.	資訊工程學系	School System / Class	大學日間部2年1班
Lecturer	高富建	Full or Part-time	專任
Required / Credit	Required / 3	Graduate Class	No
Time / Place	(四)234 /	Language	Chinese

Introduction
本課程介紹數位電路設計基本原理與技術，是資訊工程領域之基礎核心課程之一。課程涵蓋序向電路元件、基本序向電路(如計數器等)、序向電路時序分析、有限狀態機與序向電路設計流程等課題之探討，使學生建立數位邏輯電路的基本概念，了解數位邏輯電路的運作原理。

Outline
Unit 1 Latches , and Flip-Flops
Unit 2 Registers , Shift Registers , Binary Counters , and Counters for other Sequences
Unit 3 Analysis of Clocked Sequential Circuits, and State Tables and Graphs
Unit 4 Derivation of State Graphs and Tables
Unit 5 Reduction of State Tables
Unit 6 Design of Sequential Circuits

Prerequisite
數位邏輯導論、組合邏輯設計