

102-2 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	電子電路設計	Serial No. / ID	2781 / EEI4004
Dept.	電機工程學系	School System / Class	大學日間部3年3班
Lecturer	洪士賢	Full or Part-time	專任
Required / Credit	Optinal / 3	Graduate Class	No
Time / Place	(三)56 / H228 (四)2 / H228	Language	Chinese

Introduction
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Outline
chapter1 Introduction to OP AMPS chapter2 First Experiences with an OP AMP chapter3 Inverting and Noninverting Amplifiers chapter4 Comparators and Controls chapter5 Selected Applications of OP AMPS chapter6 Signal Generators chapter7 OP AMPS with Diodes chapter8 Differential, Instrumentation, and Bridge Amplifiers chapter9 DC Performance: Bias, Offsets, and Drift chapter10 AC Performance: Bandwidth, Slew rate, Noise chapter11 Active Filters

Prerequisite
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