

102-2 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	系統專題實作	Serial No. / ID	2112 / EEI4270
Dept.	電機工程學系	School System / Class	大學日間部4年1班
Lecturer	莊豐嘉	Full or Part-time	兼任
Required / Credit	Optinal / 3	Graduate Class	Yes
Time / Place	(五)ABC / H369	Language	Chinese

Introduction
It provides an opportunity to train students how to do system designs and realize them.

Outline
<ol style="list-style-type: none">1. Introduction to tools2. Computer programing3. Circuit design4. Test and improve5. Documentation

Prerequisite
Circuits, electronics