

102-1 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	VLSI電腦輔助設計	Serial No. / ID	2000 / EEI3122
Dept.	電機工程學系	School System / Class	大學日間部3年1班
Lecturer	陳慶順	Full or Part-time	專任
Required / Credit	Optinal / 3	Graduate Class	No
Time / Place	(四)678 / H371	Language	Chinese

Introduction

This course is intended to study CMOS VLSI concepts and to use IC design tools to achieve preliminary IC design ability.

Outline

UNIX Fundamentals
CMOS VLSI Design Concepts and DESIGN FLOW
SCHEMATIC、SYMBOL
DESIGN RULE CHECK
I/O CIRCUIT 及PACKAGE
SPICE SIMULATION

Prerequisite

Digital Circuits and Logic Design, Electrical Circuit Theory, Electronics, Semiconductor Processing Technology