

102-1 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	數位邏輯技術與實驗	Serial No. / ID	1971 / EEB2004
Dept.	電機工程學系	School System / Class	進修學士班2年1班
Lecturer	蔡渙良	Full or Part-time	專任
Required / Credit	Required / 3	Graduate Class	No
Time / Place	(三)BCDE / H362	Language	Chinese

Introduction
This course makes the students easily understand the digital logic circuit through experimental measurement.

Outline
1.Instruments 2.Basic logics 3.Combinational Logic 4.Binary Adder and subtractor 5.Decimal Adder 6.Comparator 7.Coder and decoder 8.Seven-segment display 9.Mux and demux 10.Flipflop

Prerequisite
Digital logic circuit