

101-2 Preliminary Syllabus, Da-Yeh Univ

| Information | | | |
|-------------------|---------------|-----------------------|----------------|
| Title | 數位邏輯實驗 | Serial No. / ID | 0775 / EEI2037 |
| Dept. | 電機工程學系 | School System / Class | 大學日間部1年1班 |
| Lecturer | 洪士賢 | Full or Part-time | 專任 |
| Required / Credit | Required / 1 | Graduate Class | No |
| Time / Place | (四)678 / H362 | Language | Chinese |

| Introduction |
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| 1.digital I.C. 2.boolean algebra and digital circuits 3.karnaugh map and digital circuit design 4.cobinational circuits design 5.sequentail logic I.C. |

| Outline |
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| boolean algebra karnaugh maps multi-level circuits combinational circuit design logic devices latches and flip-flops |

| Prerequisite |
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| digital circuit and logic design |