

# 101-1 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	半導體實驗	Serial No. / ID	1743 / EEI4208
Dept.	電機工程學系	School System / Class	大學日間部4年1班
Lecturer	張永平	Full or Part-time	兼任
Required / Credit	Required / 1	Graduate Class	Yes
Time / Place	(五)234 / H371	Language	Chinese

Introduction
To provide students with hands-on experience of semiconductor fabrication so that students can have a better understanding on the subjects of various semiconductor fabrication techniques.

Outline
Vacuum Pump and Systems; Wafer Sheet Resistance; Wafer Cleaning; Thin Film Deposition; Surface Morphology; Thermal Oxidation; Chemical Etching; Selective Doping

Prerequisite
VLSI Technology