

101-1 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	電路學實驗	Serial No. / ID	1714 / EEI2035
Dept.	電機工程學系	School System / Class	大學日間部2年1班
Lecturer	李世鴻	Full or Part-time	專任
Required / Credit	Required / 1	Graduate Class	No
Time / Place	(一)789 / H362	Language	Chinese

Introduction	
Electric Circuits Laboratory	

Outline	
Thevenin Theorem; Norton Theorem; Maximum Power Transfer; AC Voltage, Current, Power Measurement; RLC Series and Parallel Circuits.	

Prerequisite	
None	