

# 101-1 Preliminary Syllabus, Da-Yeh Univ

| Information       |                          |                       |                |
|-------------------|--------------------------|-----------------------|----------------|
| Title             | 邏輯設計                     | Serial No. / ID       | 1308 / MAV3013 |
| Dept.             | 機械與自動化工程學系               | School System / Class | 四技部3年1班        |
| Lecturer          | 張義芳                      | Full or Part-time     | 專任             |
| Required / Credit | Optinal / 3              | Graduate Class        | No             |
| Time / Place      | (三)9 / H439 (四)78 / H439 | Language              | Chinese        |

| Introduction  |
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| Logic circuit design is a fundamental course dealing with logic circuit design and development. This course will cover the principle of Boolean Algebra and Reduction Techniques. Lecture materials will provide students a clear and thorough understanding of the Boolean algebra and logic circuit design. The adequate ability to analyze the logic relation in the logic circuit system will be developed. |

| Outline   |
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| 單元主題1 : Number Systems and Codes<br>單元主題2 : Basic Logic Gates<br>單元主題3 : Boolean Algebra and Reduction Techniques<br>單元主題4 : Exclusive-OR and Exclusive-NOR Gates<br>單元主題5 : Flip-Flops and Registers |

| Prerequisite         |
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| Physics and Electric |