100-2 Preliminary Syllabus, Da-Yeh Univ

Information

Title		Serial No. / ID	1541 / EEB2022
Dept.		School System / Class	1 1
Lecturer		Full or Part-time	
Required / Credit	Required / 1	Graduate Class	No
Time / Place	( )ABC / H362	Language	Chinese

Introduction

Digital logic experiments are aimed at the completion of logic design course, students can be familiar to those devices studied in class and do the necessary experiments for future study and design.

Outline

Experiments include combinatorial circuits, adder, comparator, decoder, display, multiplexer, flip-flop, counters, shift registers.

Prerequisite

Logic Design