

100-1 Preliminary Syllabus, Da-Yeh Univ

Information			
Title	類比IC設計	Serial No. / ID	1338 / EEI4152
Dept.	電機工程學系	School System / Class	大學日間部4年1班
Lecturer	張永平	Full or Part-time	兼任
Required / Credit	Optinal / 3	Graduate Class	Yes
Time / Place	(二)9AB / H371	Language	Chinese

Introduction
1. circuit analysis and design 2. problem analysis systematically with mathmetics

Outline
Bascis Concepts Basic Laws Methods of Analysis Circuit Theorems Operational Amplifiers Capacitors and Inductors Fisrt-Order Circuits Second-Order Circuits

Prerequisite
微積分、普通物理